

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,909,305 B1  
DATED : June 21, 2005  
INVENTOR(S) : Li et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2,

Line 8, change "assumes" to -- assume --.

Line 50, before "significant" insert -- a --.

Line 52, before "wider" insert -- a --.

Column 3,

Line 62, after "power-up phase" change "that" to -- than --.

Column 8,

Line 13, after "in such" insert -- a --.

Line 47, after "significant" change "that" to -- than --.

Column 9,

Line 26, change "flip-flop" to -- flip-flops --.

Line 63, change "way," to -- ways, --.

Column 11,

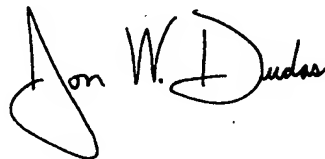
Line 14, change "process," to -- processes, --.

Column 12,

Line 36, after "input/output node," remove "width".

Signed and Sealed this

Twenty-second Day of November, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS

Director of the United States Patent and Trademark Office